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[www.cs.princeton.edu/~drh/pubs/cdb.pdf](http://www.cs.princeton.edu/~drh/pubs/cdb.pdf)

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this algorithm has been implemented in a retargetable C Compiler and, through testing, it has been found  
[cs.mtu.edu/pub/sweeny/brasier/thesis.ps](http://cs.mtu.edu/pub/sweeny/brasier/thesis.ps)

[Early Experience with ASDL in Icc - Hanson \(1999\)](#) (Correct) (2 citations)  
[www.research.microsoft.com/~drh/pubs/asdl.pdf](http://www.research.microsoft.com/~drh/pubs/asdl.pdf)

[Bottom-up Tree Rewriting Tool MBURG - John Gough \(1996\)](#) (Correct) (2 citations)  
for code selection in the widely available retargetable C compiler Icc[2]The gardens point compilers  
[www.dstc.qut.edu.au/~gough/slgsplan.ps](http://www.dstc.qut.edu.au/~gough/slgsplan.ps)

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[kasuga.csce.kyushu-u.ac.jp/~cao/paper/isiped\\_01.ps](http://kasuga.csce.kyushu-u.ac.jp/~cao/paper/isiped_01.ps)

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Indirect jump through a branch table for a retargetable C compiler is given in [14] in the following  
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are described. The structure of the ValenC retargetable compiler which we have developed and how to  
in detail. The structure of the ValenC retargetable compiler and how the correctness of ValenC  
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[Efficient code generation for ASIPs with different word sizes - Andrea Cilio \(1997\)](#) (Correct) (1 citation)  
methodology, developed on the basis of a retargetable C compiler (the SUIF compiler) requires the  
[cardit.et.tudeft.nl/MOVE/papers/Cilio97a.ps](http://cardit.et.tudeft.nl/MOVE/papers/Cilio97a.ps)

[Resume of Grigori R. Humphreys - Humphreys \(2002\)](#) (Correct)  
program, in the style of Hanson's A Retargetable C Compiler and Knuth's T E X: The Program. Drafts  
[graphics.stanford.edu/~humper/cv.pdf](http://graphics.stanford.edu/~humper/cv.pdf)

[The Icc 4.x Code-Generation Interface - Christopher Fraser And](#) (Correct)  
compiler for Standard C described in A Retargetable C Compiler: Design and Implementation. This report  
[www.research.microsoft.com/~drh/pubs/interface4.pdf](http://www.research.microsoft.com/~drh/pubs/interface4.pdf)

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[students.cs.byu.edu/~clement/cs584/TR92-15.ps](http://students.cs.byu.edu/~clement/cs584/TR92-15.ps)

[Building Real Computer Systems - Uht, Lo, Sun, Daly, Kowalski \(2000\)](#) (Correct)  
[ele.url.edu/pub/uht/leemicro2000.pdf](http://ele.url.edu/pub/uht/leemicro2000.pdf)

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[ele.url.edu/pub/uht/mug97.PS.gz](http://ele.url.edu/pub/uht/mug97.PS.gz)

[Compiler Predication - Postiff](#) (Correct)  
[2] Christopher Fraser and David Hanson, A Retargetable C Compiler: Design and Implementation. Benjamin/  
[www-personal.umich.edu/~postiff/papers/S63/report.fm.ps](http://www-personal.umich.edu/~postiff/papers/S63/report.fm.ps)

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ftp.cs.washington.edu/ut/1993/11/UW-CSE-93-11-02.PS.Z

[Retargetable Instruction Scheduling for Pipelined Processors](#) - Bradlee (1991) (Correct) (13 citations)  
[2] D. Bradlee. Retargetable Instruction Scheduling for Pipelined  
ftp.cs.washington.edu/ut/1991/08/UW-CSE-91-08-07.PS.Z

[Software Synthesis for Real-Time Information](#) - Correa, Thoen, (1995) (Correct) (11 citations)  
This can be easily achieved by means of a retargetable code generator. This is especially interesting when  
www.imec.be/vsdm/projects/sw\_synthesis/.../ftp/pub/sw\_synthesis/mc-dagstuhl95.ps

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this problem. Mimola [2] contains a retargetable code generator for a class of non-DSP architectures.  
ftp.imec.be/pub/vsdm/reports/retargetable\_code\_generation/gg-jvsp94.ps.gz

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description language. Afterwards, a retargetable code generator is described. With this tool it is  
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nML is presented along with the retargetable code generator Cbc and the instruction set simulation  
www.techfak.uni-bielefeld.de/techfak/ag5/ti/forschung/publikationen/codegen.ps.gz

[Instruction Selection, Resource Allocation, and Scheduling](#) - Hanono, Devadas (1998) (Correct) (3 citations)  
Allocation, and Scheduling in the AVIV Retargetable Code Generator Silvina Hanono Srinivas Devadas  
glen.ics.mit.edu/~dovadas/pubs/aviv.ps

[Machine-Independent Support for Garbage Collection, Debugging, ...](#) - Jones, Ramsey (1998) (Correct) (2 citations)  
is hard work. One might try to reuse a retargetable code generator from another compiler, but code  
ftp.cs.virginia.edu/pub/techreports/CS-98-19.ps.Z

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ftp-date.uni-paderborn.de/pub/people/weper/paper/TKW.ps.gz

[A Retargetable Code Generator for SUIF](#) - Markwardt, Schulz (1996) (Correct) (1 citation)  
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sulf.stanford.edu/sulfconf/sulfconf1/papers/paper1.ps

[Aviv: A Retargetable Code Generator for Embedded Processors](#) - Hanono (1999) (Correct) (1 citation)  
Aviv: A Retargetable Code Generator for Embedded Processors by Silvina  
caa.ics.mit.edu/~silvina/thesis/phd\_thesis.ps.gz

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simultaneously. Work with the Marion retargetable code generator [3] has shown that for inorder  
ftp.cc.gatech.edu/pub/coc/tech\_reports/1997/GIT-CC-97-34.ps.Z

[Design and Implementation of the GNU INSEL-Compiler gic](#) - Plizka (1997) (Correct) (1 citation)  
. 10.2.1.2 gcc as a Retargetable CodeGenerator.  
wwwspies.informatik.tu-muenchen.de/personen/plizka/gic.ps.gz

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[Heuristic Tradeoffs Between Latency And Energy](#), - Anand, Acosta, De Veciana (2000) (Correct)  
www.sigda.org/Archives/ProceedingArchives/Codes/Codes2000/papers/2000/codes00/htmfiles/SUN\_SGI/./pdffiles/06\_2.pdf

[Publications of Leon Moonen - Moonen](#) (Correct)  
www.cwi.nl/~leon/papers/publications\_leon\_moonen.ps.gz

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